

CAMBOARD ELECTRONICS



Entwicklung und Vertrieb
elektronischer Produkte

BVTBC10

Quantization: 10 bit
Processing: 4:2:2 component digital
SMPTE RP125 & CCIR 656
Sampling: 2x (27MHz) ADC decoding
Sampling: 2x (27MHz) DAC encoding
SDI Pipeline: 4:2:2 SMPTE 259M-C (270 Mbps)
Jitter: .15UI 1pf >10kHz SDI Output
Jitter Genlock: < 50nS H- phase
Genlock Range: -11uS to +7uS H- Phase
Proc Range: -40 to + 6dB (\pm 50 IRE Brightness)
Signal to noise: > 62dB rms BW= 30MHz
Differential phase: < 1° @ 4.43 MHz
Differential gain: < 1% @ 4.43 MHz
K-Factor: < 1% (2T Pulse)
Y/ C delay: < 10ns
Processing delay: 2-Fields (+ 0,- 1) Max
Frequency response
Sync mode: Flat to 5.5 MHz, \pm .15dB
TBC mode: Flat to 5.5 MHz, \pm .15dB
CV/Y-C/SDI: Flat to 5.5 MHz, \pm .15dB
Decoder: Adaptive 2-line 10-bit digital COMB filter with exclusive SURELOCK for solid pictures from unstable sources



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